# Processes are in the Eye of the Beholder

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### Author's Abstract

A two-process algorithm is shown to be equivalent to an N-process one, illustrating the insubstantiality of processes. A formal equivalence proof in TLA (the Temporal Logic of Actions) is sketched.

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## 1 Introduction

Processes are often taken to be the fundamental building blocks of concurrency. A concurrent algorithm is traditionally represented as the composition of processes. We show by an example that processes are an artifact of how an algorithm is represented. The difference between a two-process representation and a four-process representation of the same algorithm is no more fundamental than the difference between 2 + 2 and 1 + 1 + 1 + 1.

Our example is a fifo ring buffer, pictured in Figure 1. The *i*th input value received on channel *in* is stored in  $buf[i-1 \mod N]$ , until it is sent on channel *out*. Input and output may occur concurrently, but input is enabled only when the buffer is not full, and output is enabled only when the buffer is not full, and output is enabled only when the buffer is not full.

Figure 2 shows a representation of the ring buffer as a two-process program in a CSP-like language [2]. (We ignore CSP's termination convention; the loops are assumed never to terminate.) The variables p and g record the number of values received on channel *in* by the *Receiver* process and sent on channel *out* by the *Sender* process, respectively. Declaring p and g to be internal means that their values are not externally visible, so a compiler is free to implement them any way it can, or to eliminate them entirely.

The intuitive meaning of this program should be clear to readers acquainted with CSP. We will not attempt to give a rigorous meaning to the program text. Programming languages evolved as a method of describing algorithms to compilers, not as a method for reasoning about them. We do not know how to write a completely formal proof that two programminglanguage representations of the ring buffer are equivalent. In Section 2, we represent the program formally in TLA, the Temporal Logic of Actions [5]. Figure 2 will serve only as an intuitive description of the TLA formula.

Figure 3 shows another representation of the ring buffer, where IsNext

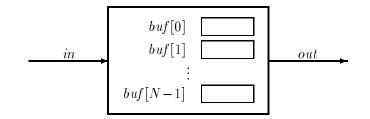


Figure 1: A ring buffer.

$$\begin{array}{l} \text{in, out : channel of } Value \\ \text{buf array } 0 \dots N-1 \text{ of } Value \\ p, g: \text{ internal } Natural \text{ initially } 0 \\ \text{Receiver :: } * \begin{bmatrix} p-g \neq N & \rightarrow & in? \text{ buf } [p \ mod \ N]; \\ p := p+1 \end{bmatrix} \\ || \\ \text{Sender :: } * \begin{bmatrix} p-g \neq 0 & \rightarrow & out \ ! \ buf [g \ mod \ N]; \\ g := g+1 \end{bmatrix} \end{array}$$

Figure 2: The ring buffer, represented in a CSP-like language.

Figure 3: Another representation of the ring buffer.

p	pp[0]	pp[1]	pp[2]	pp[3]
0	0	0	0	0
1	1	0	0	0
2	1	1	0	0
3	1	1	1	0
4	1	1	1	1
5	0	1	1	1
6	0	0	1	1
÷	:	•	:	:

Figure 4: The correspondence between values of pp and p, for N = 4.

is defined by

$$IsNext(r, i) \triangleq if i = 0 then r[0] = r[N-1]$$
  
else  $r[i] \neq r[i-1]$ 

This is as an N-process program; the *i*th process, Buffer(i), reads and writes buf[i]. Variables p and g of the two-process program are replaced by arrays pp and gg of bits. Array elements pp[i] and gg[i] are read and written by process Buffer(i), and are read by process  $Buffer(i+1 \mod N)$ .

The two programs are equivalent because the values assumed by pp and gg in the N-process program correspond directly to the values assumed by p and g in the two-process one. The correspondence between pp and p is shown in Figure 4 for N = 4. A boxed number in the pp[i] column indicates that IsNext(pp, i) equals TRUE. The correspondence between gg and g is the same.

It is not hard to argue informally that the two programs are equivalent. Formalizing this argument should be as straightforward as proving formally that 222 + 222 equals 111 + 111 + 111 + 111. But, even if straightforward, a completely formal proof of either result from first principles is not trivial. In Section 3, we sketch a formal TLA proof that the two versions of the ring buffer are equivalent.

## 2 The Algorithm in TLA

We now write the TLA formulas that describe the programs of Figures 2 and 3. The program texts do not tell us what liveness properties are

assumed. To make the example more interesting, we assume no liveness properties for sending values on the *in* channel, but we require that every value received in the buffer be eventually sent on the *out* channel. For the two-process program, this means assuming fairness for the *Sender*, but not for the *Receiver*. For the *N*-process program, it means assuming fairness for the *full* action of each process, but not for the *empty* action.

The program texts also do not determine the grain of atomicity. For simplicity, we assume that an entire guarded command is a single atomic operation. Thus, evaluating a guard and executing the subsequent communication and assignment statements is taken to be an indivisible step.

We give an interleaving representation of the ring buffer—one in which sending and receiving are represented by distinct atomic actions. In Section 4, we describe how the specifications and proofs could be written in terms of a noninterleaving representation that allows values to be sent and received simultaneously.

We use the following notation:  $\mathcal{N}$  is the set of natural numbers;  $\mathcal{Z}_m$  is the set  $\{0, \ldots, m-1\}$ ; square brackets denote function application;  $[S \to T]$  is the set of functions with domain S and range a subset of T;  $[i \in S \mapsto e]$  is the function f with domain S such that f[i] = e for all  $i \in S$ ; [f EXCEPT ![i] = e] is the function  $\hat{f}$  that is the same as f except  $\hat{f}[i] = e$ ; angle brackets enclose tuples; t[i] is the *i*th component of tuple t, so  $\langle v, w \rangle [2] = w$ ; and  $S \setminus T$  is the set of elements in S that are not in T.

A TLA formula is an assertion about *behaviors*, which are sequences of states. Steps (pairs of successive states) in a behavior are described by *actions*, which are boolean-valued expressions containing primed and unprimed variables; unprimed variables refer to the old state and primed variables refer to the new state. To describe CSP-style communication, we represent a channel by a variable and represent the sending of a value by a change to that variable. We define Channel(V) to be the set of legal values of a channel of type V, and Comm(v, c) to be the action that represents communicating a value v on channel c. The actual definitions, given below, are irrelevant; we require only that a Comm(v, c) action changes c, if  $v \in V$ and  $c \in Channel(V)$ .

$$Channel(V) \triangleq V \times \mathcal{Z}_2$$
$$Comm(v, c) \triangleq c' = \langle v, 1 - c[2] \rangle$$

The TLA formula  $\Pi_2$  that represents the two-process program is defined in Figure 5. We now explain that definition.

$$Type2 \triangleq \land p, g \in \mathcal{N} \\ \land buf \in [\mathcal{Z}_N \to Value] \\ \land in, out \in Channel(Value) \\ UnB(i) \triangleq [j \in \mathcal{Z}_N \setminus \{i\} \mapsto buf[j]] \\ Rcv \triangleq \land p - g \neq N \\ \land p' = p + 1 \\ \land Comm(buf'[p \mod N], in) \\ \land UNCHANGED \langle g, out, UnB(p \mod N) \rangle \\ Snd \triangleq \land p - g \neq 0 \\ \land g' = g + 1 \\ \land Comm(buf[g \mod N], out) \\ \land UNCHANGED \langle p, buf, in \rangle \\ \Phi_2 \triangleq \land \Box Type2 \\ \land (p = 0) \land \Box[Rcv]_{\langle p, buf, in \rangle} \\ \land (g = 0) \land \Box[Snd]_{\langle g, out \rangle} \land WF_{\langle g, out \rangle}(Snd) \\ \Pi_2 \triangleq \exists p, q : \Phi_2$$



A list of expressions bulleted by  $\wedge$  denotes their conjunction; indentation is used to eliminate parentheses. If formula F is written as such a list, then F.i is its *i*th conjunct—for example, Rcv.2 is p' = p+1. A similar convention is used for disjunctions.

The state predicate Type2 asserts that each variable has the correct type. (The array variable *buf* of the programming language representation becomes a variable whose value is a function.) The type declarations of the two-process program are represented by the TLA formula  $\Box Type2$ , which asserts that Type2 equals TRUE in all states of the behavior.

Action Snd describes a step of the Sender process; it can occur only when  $p - g \neq 0$ , and it increments g by 1, communicates  $buf[g \mod N]$  on channel out, and leaves p, buf, and in unchanged (UNCHANGED v is defined to equal v' = v). Similarly, action Rcv describes a step of the Receiver process. The conjunct Rcv.3 asserts that the value  $buf'[p \mod N]$  (the new value of  $buf[p \mod N]$ ) is communicated on channel in. The state function UnB(i) is defined so that, if it is unchanged, then buf[j] is unchanged for all  $j \neq i$ . Thus, Rcv asserts that the new value of  $buf[p \mod N]$  is the value communicated on channel in, and that buf[j] remains unchanged for all  $j \neq p \mod N$ . Formula  $\Phi_2.2$  describes the *Receiver* process. It asserts that p is initially 0, and that every step is a Rcv step or leaves p, buf, and in unchanged  $([A]_v)$  is defined to equal  $A \vee (v' = v)$ . Steps that leave p, buf, and in unchanged represent steps of the *Receiver*'s environment—either steps of the *Sender* or steps of the entire program's environment. The conjunct  $\Phi_2.3$  similarly represents the *Sender* process. The formula  $WF_{\langle g, out \rangle}(Snd)$  asserts weak fairness of the *Snd* action. In general,  $WF_v(A)$  asserts that if action  $\langle A \rangle_v$  (defined to equal  $A \wedge (v' \neq v)$ ) remains continuously enabled, then an  $\langle A \rangle_v$  step must eventually occur.

Formula  $\Phi_2$  is the conjunction of the specifications of the two processes with the formula asserting type correctness. It describes the two-process program with p and g visible. The complete program specification  $\Pi_2$  is obtained by hiding p and g. In logic, hiding means existential quantification; in temporal logic, flexible variables (distinct from rigid variables like N) are hidden with the temporal existential quantifier  $\exists$ .

The conjunct  $\Box Type2$  of  $\Phi_2$  makes type correctness an explicit part of the specification. We put type-correctness assumptions in our specifications to make them as much like Figures 2 and 3 as possible. However, to avoid errors, it is usually better to let type correctness be a consequence of the specification. We could rewrite  $\Phi_2$  as follows to eliminate the conjunct  $\Box Type2$ . The conjunct  $\Box Type2.1$  is already redundant because it is implied by  $\Phi_2.2 \land \Phi_2.3$ . We can eliminate  $\Box Type2.3$  by making Type2.3 part of the initial condition, since  $Type2.3 \land \Phi_2.2 \land \Phi_2.3$  implies  $\Box Type2.3$ . (The proof requires the fact that  $c \in Channel(V)$  and Comm(v, c) imply  $c' \in Channel(V)$ .) We can eliminate  $\Box Type2.2$  in the same way, if we modify Rcv so it leaves the domain of buf unchanged.

The TLA formula  $\Pi_N$  that represents the N-process program is defined in Figure 6. There are two things in this definition that merit further explanation. First, we introduce an array *ctl* to represent the control state. The value of ctl[i] equals "**empty**" if control in process Buffer(i) is at the point labeled *empty*, and it equals "full" if control is at *full*. Second, we introduce an action NotProc(i) that has no obvious counterpart in Figure 3 or in  $\Pi_2$ . The specifications of the two processes in Figure 2 are especially simple because each variable is changed by an action of only one of the processes. For example, a step of the *Sender*'s environment can be characterized as any step that leaves g and *out* unchanged. We can think of g and *out* as belonging to the *Sender*. In the N-process program, pp[i], gg[i], and ctl[i] belong to Buffer(i). However, *in* and *out* don't belong to any single process; they can be changed by a step of any of the N pro-

Figure 6: The TLA formula  $\Pi_{\rm N}$  representing the N-process program.

cesses. The variable *in* belongs to Buffer(i) only when IsNext(pp, i) equals TRUE, and *out* belongs to Buffer(i) only when IsNext(gg, i) equals TRUE. Action NotProc(i) characterizes steps of Buffer(i)'s environment, which is allowed to change *in* when IsNext(pp, i) equals FALSE, and to change *out* when IsNext(gg, i) equals FALSE. The subscript in  $\Box[\ldots]_{varN}$  allows steps of the entire program's environment that leave all the variables unchanged. It is semantically superfluous, since NotProc(i) already allows such steps, but the syntax of TLA requires some subscript.

## 3 The Proof

We now give a hierarchically structured proof that  $\Pi_2$  and  $\Pi_N$  are equivalent [4]. The proof is completely formal, meaning that each step is a mathematical formula. English is used only to explain the low-level reasoning. The entire proof could be carried down to a level at which each step follows from the simple application of formal rules, but such a detailed proof is more suitable for machine checking than human reading. Our complete proof, with "Q.E.D." steps and low-level reasoning omitted, appears in Appendix A.

The correctness of the algorithm rests on simple properties of integers and of the *mod* operator. We need the following lemma, where the bit array Rep(m) used to represent the integer m is defined by

 $Rep(m) \triangleq [i \in \mathcal{Z}_N \mapsto \mathbf{if} \ i < m \mod 2N \le i + N \mathbf{then} \ 1 \mathbf{else} \ 0]$ 

The lemma is proved in Appendix B. We assume throughout that N is a positive integer.

**Lemma 1** If  $m \in \mathcal{N}$  and  $i \in \mathcal{Z}_N$ , then

- 1.  $IsNext(Rep(m), i) \equiv (i = m \mod N)$
- 2.  $IsNext(Rep(m), i) \Rightarrow$ Rep(m+1) = [Rep(m) Except ![i] = 1 - Rep(m)[i]]

For temporal reasoning, we use the following TLA rules from Figure 5 of [5].

**Theorem**  $\Pi_2 \equiv \Pi_N$ 1a.  $\Phi_2 \equiv \Phi_2^u$ b.  $\Phi_N \equiv \Phi_N^u$ 2a.  $\Phi_2^{\rm u} \equiv \exists pp, gg, ctl : \Phi_2^{\rm h}$ b.  $\Phi_{\rm N}^{\rm u} \equiv \exists p, g : \Phi_{\rm N}^{\rm h}$ 3.  $\Phi_2^h \equiv \Phi_N^h$ 4. Q.E.D. Proof:  $\Pi_2 \equiv \mathbf{J} p, g : \Phi_2^{\mathrm{u}}$ step 1a and the definition of  $\Pi_2$  $\equiv \exists p, g, pp, gg, ctl : \Phi_2^{\rm h}$ step 2a  $= \mathbf{J} p, g, pp, gg, ctl : \Phi_{\mathbf{N}}^{\tilde{\mathbf{h}}} \\ = \mathbf{J} pp, gg, ctl, p, g : \Phi_{\mathbf{N}}^{\tilde{\mathbf{h}}}$ step 3 simple logic  $\equiv \ \, {\bf \exists} \ \, pp\,,gg\,,ctl \ : \ \, \Phi^{\rm u}_{\rm N}$ step 2b step 1b and the definition of  $\Pi_{\rm N}$  $\equiv \Pi_{N}$ 

Figure 7: The high-level structure of the proof.

(This version of TLA2 generalizes the one in [5].)

The high-level structure of the proof is shown in Figure 7. The proofs of steps 1–3, and the definitions of  $\Phi_2^u$ ,  $\Phi_N^u$ ,  $\Phi_2^h$ , and  $\Phi_N^h$ , are given in the following sections.

#### 3.1 Step 1: Removing the Process Structure

Formulas  $\Phi_2^u$  and  $\Phi_N^u$  are defined in Figure 8. They can be thought of as uniprocess versions of the two algorithms. We obtained them by rewriting  $\Phi_2$  and  $\Phi_N$  as formulas with a single next-state relation, instead of as the conjunction of processes.

Step 1a is proved as follows.

1a.  $\Phi_2 \equiv \Phi_2^u$ 1a.1.  $Type2 \Rightarrow ([Rcv]_{\langle p, buf, in \rangle} \land [Snd]_{\langle g, out \rangle} \equiv [Rcv \lor Snd]_{var2})$ PROOF: Given below.

$$\begin{array}{rcl} var2 & \triangleq & \langle p, g, buf, in, out \rangle \\ \Phi_2^{\mathrm{u}} & \triangleq & \wedge \Box Type2 \\ & & \wedge (p=0) \wedge (g=0) \\ & & \wedge \Box [Rcv \vee Snd]_{var2} \\ & & \wedge \mathrm{WF}_{\langle g, out \rangle}(Snd) \\ \Phi_N^{\mathrm{u}} & \triangleq & \wedge \Box TypeN \\ & & \wedge \forall i \in \mathcal{Z}_N : (pp[i] = gg[i] = 0) \wedge (ctl[i] = \text{``empty''}) \\ & & \wedge \Box [\exists i \in \mathcal{Z}_N : Fill(i) \vee Empty(i)]_{varN} \\ & & \wedge \forall i \in \mathcal{Z}_N : \mathrm{WF}_{varN}(Empty(i)) \end{array}$$

Figure 8: Formulas  $\Phi_2^{\rm u}$  and  $\Phi_{\rm N}^{\rm u}$ .

1a.2.  $\Box Type2 \Rightarrow (\Box [Rcv]_{(p,buf,in)} \land \Box [Snd]_{(g,out)} \equiv \Box [Rcv \lor Snd]_{var2})$ PROOF: Step 1a.1 and rule TLA2.

1a.3. Q.E.D.

**PROOF:** Step 1a.2 and the definitions of  $\Phi_2$  and  $\Phi_2^u$ .

Step 1a.1 is proved by showing that Type2 implies

 $[Rcv]_{\langle p, buf, in \rangle} \land [Snd]_{\langle g, out \rangle}$  $\equiv$  by definition of  $[A]_v$  $\wedge \ \mathit{Rcv} \lor (\langle \mathit{p}, \mathit{buf}, \mathit{in} \rangle' = \langle \mathit{p}, \mathit{buf}, \mathit{in} \rangle)$  $\land Snd \lor (\langle g, out \rangle' = \langle g, out \rangle)$  $\equiv$  by propositional logic  $\lor \ Rcv \land (\langle g, out \rangle' = \langle g, out \rangle)$  $\lor$  Snd  $\land$  ( $\langle p, buf, in \rangle' = \langle p, buf, in \rangle$ )  $\lor$  Rev  $\land$  Snd  $\lor \; (\langle g, out \rangle' = \langle g, out \rangle) \land (\langle p, buf, in \rangle' = \langle p, buf, in \rangle)$  $\equiv \lor Rcv$ *Rev* implies  $\langle g, out \rangle' = \langle g, out \rangle$  $\lor$  Snd Snd implies  $\langle p, buf, in \rangle' = \langle p, buf, in \rangle$  $\lor$  False  $Type_2 \wedge Rcv$  implies  $p' \neq p$ , and Snd implies p' = p $\lor var2' = var2$  $\langle v_1, \ldots, v_m \rangle' = \langle v_1, \ldots, v_m \rangle$  iff  $(v'_1 = v_1) \land \ldots \land (v'_m = v_m)$  $\equiv [Rcv \lor Snd]_{var2}$ by definition of  $[A]_v$ 

All of the nontemporal steps in our proof can be reduced to this kind of algebraic manipulation. From now on, we just sketch such proofs and leave the detailed calculations to the reader.

The proof of step 1b is similar to that of step 1a, but it is a bit more difficult because it requires an invariant InvN, which asserts that the arrays

pp and gg are representations of natural numbers.

 $InvN \stackrel{\Delta}{=} (\exists m \in \mathcal{N} : pp = Rep(m)) \land (\exists m \in \mathcal{N} : gg = Rep(m))$ 1b.  $\Phi_N \equiv \Phi_N^u$ 1b.1a.  $\Phi_N \Rightarrow \Box InvN$ b.  $\Phi_{\rm N}^{\rm u} \Rightarrow \Box InvN$ **PROOF**: Described below. 1b.2.  $TypeN \wedge InvN \Rightarrow$  $[\exists i \in \mathcal{Z}_N : Fill(i) \lor Empty(i)]_{varN} \equiv$  $\forall i \in \mathcal{Z}_N : [Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}$ **PROOF:** If  $i \neq j$ , then TypeN implies that  $Fill(i) \wedge Fill(j)$ ,  $Empty(i) \wedge$ Empty(j), and  $Fill(i) \wedge Empty(j)$  are all false; and  $TypeN \wedge InvN$  implies  $Fill(i) \Rightarrow NotProc(j)$  and  $Empty(i) \Rightarrow NotProc(j)$ . By Lemma 1.1,  $TypeN \land InvN$  implies  $(\forall i \in \mathcal{Z}_N : NotProc(i)) \equiv (varN' = varN)$ . 1b.3.  $\Box$  TypeN  $\land \Box$  InvN  $\Rightarrow$  $\Box [\exists i \in \mathcal{Z}_N : Fill(i) \lor Empty(i)]_{varN} \equiv$  $\forall i \in \mathcal{Z}_N : \Box[Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}$ **PROOF**: Step 1b.2 and rule TLA2.

#### 1b.4 Q.E.D.

**PROOF**: Steps 1b.1 and 1b.3 and the definitions of  $\Phi_N$  and  $\Phi_N^u$ .

Steps 1b.1a and 1b.1b are standard invariance properties; 1b.1a is proved as follows.

1b.1a  $\Phi_N \Rightarrow \Box InvN$ 1b.1a.1 TypeN  $\land$  ( $\forall i \in \mathbb{Z}_N : pp[i] = gg[i] = 0$ )  $\Rightarrow InvN$ PROOF:  $Rep(0) = [i \in \mathcal{Z}_N \mapsto 0]$ 1b.1a.2  $\wedge$  InvN  $\wedge [TypeN \land (\forall i \in \mathcal{Z}_N : Fill(i) \lor Empty(i) \lor NotProc(i))]_{varN}$  $\Rightarrow InvN'$ 1b.1a.2.1.  $InvN \wedge TypeN \wedge (i \in \mathcal{Z}_N) \wedge Fill(i) \Rightarrow InvN'$ 1b.1a.2.2.  $InvN \wedge TypeN \wedge (i \in \mathcal{Z}_N) \wedge Empty(i) \Rightarrow InvN'$ 1b.1a.2.3.  $InvN \land TypeN \land (\forall i \in \mathbb{Z}_N : NotProc(i)) \Rightarrow InvN'$ 1b.1a.2.4.  $InvN \land (varN' = varN) \Rightarrow InvN'$ 1b.1a.2.5. Q.E.D PROOF: Steps 1b.1a.2.1-1b.1a.2.4. 1b.1a.3.  $\land$  TypeN  $\land$  ( $\forall i \in \mathbb{Z}_N : pp[i] = gg[i] = 0$ )  $\land \Box TypeN$  $\land \Box [\forall i \in \mathcal{Z}_N : Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}$  $\Rightarrow \Box InvN$ PROOF: Steps 1b.1a.1 and 1b.1a.2 and rules INV1 and INV2. 1b.1a.4. Q.E.D.

**PROOF:** Step 1b.1a.3 and rule TLA2, since  $(\forall i : [A_i]_v) \equiv [\forall i : A_i]_v$ .

Steps 1b.1a.2.1 and 1b.1a.2.2 are proved using Lemma 1.2; steps 1b.1a.2.3 and 1b.1a.2.4 follow because their hypotheses imply pp' = pp and gg' = gg. As indicated in the appendix, the proof of step 1b.1b is similar.

#### 3.2 Step 2: Adding History Variables

Formulas  $\Phi_2^{\rm h}$  and  $\Phi_N^{\rm h}$  are defined in Figure 9, which also defines their safety parts,  $\Phi_2^{\rm hS}$  and  $\Phi_N^{\rm hS}$ . We obtained  $\Phi_2^{\rm h}$  by adding pp, gg, and ctl as history variables to  $\Phi_2^{\rm u}$ ; and we obtained  $\Phi_N^{\rm h}$  by adding p and g as history variables to  $\Phi_N^{\rm u}$ . In general, adding an auxiliary variable a to a formula F means writing a formula  $F^a$  such that  $F \equiv \exists a : F^a$ . A history variable is an auxiliary variable that records information from previous states. It is added by using the following lemma, which can be deduced from the results in [1]. Step 2 is easily proved by repeated application of this lemma.

**Lemma 2 (History Variable)** If h and h' do not occur in Init,  $A_i$ ,  $B_j$ , v, or f, and h' does not occur in  $g_i$ , for all  $i \in I$  and  $j \in J$ , then

$$Init \land \Box[\exists i \in I : \mathcal{A}_i]_v \land (\forall j \in J : WF_v(\mathcal{B}_j)) \\ \equiv \exists h : \land Init \land (h = f) \\ \land \Box[\exists i \in I : \mathcal{A}_i \land (h' = g_i)]_{\langle h, v \rangle} \\ \land \forall j \in J : WF_v(\mathcal{B}_j)$$

## **3.3** Step 3: Equivalence of $\Phi_2^h$ and $\Phi_N^h$

In the two-process algorithm, p and g are the actual internal variables, while pp, gg, and ctl are history variables. The situation is reversed in the N-process algorithm. Step 3 involves showing that the history variables of one algorithm behave like the internal variables of the other. Its proof uses the following formulas, where Inv will be shown to be an invariant of both  $\Phi_2^h$  and  $\Phi_N^h$ .

$$\begin{split} IsFull(g, p, i) &\triangleq \exists m \in \mathcal{N} : (g \leq m < p) \land (i = m \mod N) \\ Inv &\triangleq \land pp = Rep(p) \\ \land gg = Rep(g) \\ \land ctl = [i \in \mathcal{Z}_N \mapsto \text{if } IsFull(g, p, i) \text{ then "full" else "empty"}] \\ \land 0 \leq p - g \leq N \end{split}$$

The high-level structure of the proof is:

Init  $\triangleq \land p = q = 0$  $\land pp = gg = [i \in \mathcal{Z}_N \mapsto 0]$  $\land ctl = [i \in \mathcal{Z}_N \mapsto \text{``empty''}]$  $Type \triangleq Type2 \land TypeN$  $\stackrel{\triangle}{=} \langle pp, gg, ctl, p, g, buf, in, out \rangle$ var $HRcv \stackrel{\Delta}{=} \wedge Rcv$  $\wedge pp' = [pp \text{ EXCEPT } ! [p \mod N] = 1 - pp[p \mod N]]$  $\wedge ctl' = [ctl \text{ except } ! [p \mod N] = "full"]$  $\wedge$  UNCHANGED gg $HSnd \triangleq \wedge Snd$  $\wedge gg' = [gg \text{ EXCEPT } ! [g \mod N] = 1 - gg[g \mod N]]$  $\wedge ctl' = [ctl \text{ EXCEPT } ![g \mod N] = "empty"]$  $\wedge$  unchanged pp $\Phi_2^{hS} \stackrel{\Delta}{=} \wedge \Box Type$  $\land$  Init  $\wedge \Box [HRcv \lor HSnd]_{var}$  $\stackrel{\scriptscriptstyle \Delta}{=} \quad \Phi_2^{\mathrm{hS}} \wedge \mathrm{WF}_{\langle g, out \rangle}(Snd)$  $\Phi^{
m h}_2$  $\stackrel{\Delta}{=} \wedge Fill(i)$ HFill(i) $\wedge p' = p + 1$  $\wedge$  UNCHANGED g $HEmpty(i) \triangleq \wedge Empty(i)$  $\wedge g' = g + 1$  $\land$  UNCHANGED p $\Phi_{\rm N}^{\rm hS} \stackrel{\Delta}{=} \wedge \Box Type$  $\land$  Init  $\land \Box[\exists i \in \mathcal{Z}_N : HFill(i) \lor HEmpty(i)]_{var}$  $\stackrel{\Delta}{=} \Phi_{\mathrm{N}}^{\mathrm{hS}} \land (\forall i \in \mathcal{Z}_{N} : \mathrm{WF}_{varN}(Empty(i)))$  $\Phi^{\rm h}_{\scriptscriptstyle \rm N}$ 

Figure 9: Formulas  $\Phi_2^h$  and  $\Phi_N^h$ .

3.  $\Phi_2^{h} \equiv \Phi_N^{h}$ 3.1a.  $Type \wedge Inv \Rightarrow (HRcv \equiv \exists i \in \mathcal{Z}_N : HFill(i))$ b.  $Type \wedge Inv \Rightarrow (HSnd \equiv \exists i \in \mathcal{Z}_N : HEmpty(i))$ 3.2.  $[Type \wedge Inv \wedge (HRcv \lor HSnd)]_{var} \equiv [Type \wedge Inv \wedge (\exists i \in \mathcal{Z}_N : HFill(i) \lor HEmpty(i))]_{var}$ 3.3a.  $\Phi_2^{hS} \Rightarrow \Box Inv$ b.  $\Phi_N^{hS} \Rightarrow \Box Inv$ 3.4.  $\Phi_2^{hS} \equiv \Phi_N^{hS}$ 3.5.  $\Box Inv \wedge \Phi_N^{hS} \Rightarrow (WF_{(g,out)}(Snd) \equiv (\forall i \in \mathcal{Z}_N : WF_{varN}(Empty(i))))$ 3.6. Q.E.D. PROOF: Immediate from steps 3.3-3.5.

Steps 3.1 and 3.2 are (nontemporal) action formulas. They make it intuitively clear why the two transformed formulas are equivalent. Step 3.1a is proved as follows.

3.1a.  $Type \land Inv \Rightarrow (HRcv \equiv \exists i \in \mathcal{Z}_N : HFill(i))$ 3.1a.1  $Type \land Inv \Rightarrow (HRcv \equiv HFill(p \mod N))$ 3.1a.1.1.  $Type \land Inv \Rightarrow ((p - g \neq N) \equiv (ctl[p \mod N] = "empty"))$ PROOF: Arithmetic reasoning and the definition of IsFull. 3.1a.1.2.  $Type \land Inv \Rightarrow IsNext(pp, p \mod N)$ PROOF: Lemma 1.1. 3.1a.1.3. Q.E.D. PROOF: Steps 3.1a.1.1 and 3.1a.1.2, and the definitions of HRcv and HFill. 3.1a.2  $Type \land Inv \Rightarrow (HFill(p \mod N) \equiv (\exists i \in \mathcal{Z}_N : HFill(i)))$ PROOF: By Lemma 1.1,  $Type \land Inv$  implies  $IsNext(pp, p \mod N)$  and  $\neg IsNext(pp, i)$ , if  $i \in \mathcal{Z}_N$  and  $i \neq (p \mod N)$ . 3.1a.3 Q.E.D.

PROOF: Steps 3.1a.1 and 3.1a.2.

As indicated in the appendix, the proof of 3.1b is analogous. Step 3.2 follows easily from step 3.1.

Step 3.3 asserts that Inv is an invariant of both formulas; its proof is a standard invariance argument.

3.3a. 
$$\Phi_{N}^{hS} \Rightarrow \Box Inv$$
  
b.  $\Phi_{N}^{hS} \Rightarrow \Box Inv$   
3.3.1. Init  $\Rightarrow$  Inv  
PROOF:  $Rep(0)$  equals  $[i \in \mathcal{Z}_{N} \mapsto 0]$  and  $IsFull(0, 0, i) \equiv$  FALSE, for  
all  $i \in \mathcal{Z}_{N}$ .

- 3.3.2a.  $Inv \wedge [Type \wedge (HRcv \vee HSnd)]_{var} \Rightarrow Inv'$ b.  $Inv \wedge [Type \wedge (\exists i \in \mathbb{Z}_N : HFill(i) \vee HEmpty(i))]_{var} \Rightarrow Inv'$ PROOF: Given below.
- 3.3.3. Q.E.D. PROOF: Steps 3.3.1 and 3.3.2, and rules INV1 and INV2.

Step 3.3.2 asserts that the next-state actions leave Inv invariant. The proof of 3.3.2a is:

3.3.2a.  $Inv \wedge [Type \wedge (HRcv \vee HSnd)]_{var} \Rightarrow Inv'$ 3.3.2a.1  $Inv \wedge Type \wedge HRcv \Rightarrow Inv'$ 

**PROOF:** Assume  $Inv \wedge Type \wedge HRcv$ . Then Inv.1' is immediate because p' = p and pp' = pp; Inv.2' follows from Lemma 1.2; Inv.4' follows from Inv.4, since HRcv implies p' = p + 1, g' = g, and  $p - g \neq N$ ; and Inv.3' holds because

$$\begin{aligned} IsFull(g', p', i) &\equiv \text{ by definition of } HRcv \\ & IsFull(g, p+1, i) \\ &\equiv \text{ by definition of } IsFull \\ &\exists m \in \mathcal{N} : (g \leq m < p+1) \land (i = m \mod N) \\ &\equiv \text{ by } Rcv.1 \text{ and } Inv.4 \\ & \text{ if } i = p \mod N \text{ then } \text{ TRUE } \text{ else } IsFull(g, p, i) \end{aligned}$$

3.3.2a.2  $\mathit{Inv} \land \mathit{Type} \land \mathit{HSnd} \Rightarrow \mathit{Inv'}$ 

**PROOF**: Similar to the proof of 3.3.2a.1.

3.3.2a.3  $Inv \land (var' = var) \Rightarrow Inv'$ PROOF: Immediate.

3.3.2a.4 Q.E.D.

PROOF: Steps 3.3.2a.1-3.3.2a.3.

Step 3.3.2b follows from steps 3.3.2a and 3.2. This completes the proof of step 3.3.

Steps 3.4 and 3.5, assert the equivalence of the safety and liveness parts of the formulas, respectively. Step 3.4 follows from 3.3 and

 $\Box Type \land \Box Inv \Rightarrow \\ \Box [HRcv \lor HSnd]_{var} \equiv \Box [\exists i \in \mathcal{Z}_N : HFill(i) \lor HEmpty(i)]_{var}$ 

which follows from step 3.2 and rule TLA2. Step 3.5 has the following high-level proof.

3.5. 
$$\Box Inv \wedge \Phi_{N}^{hS} \Rightarrow (WF_{(g,out)}(Snd) \equiv (\forall i \in \mathcal{Z}_{N} : WF_{varN}(Empty(i))))$$
  
3.5.1. 
$$\Box Inv \wedge \Phi_{N}^{hS} \Rightarrow (\forall i \in \mathcal{Z}_{N} : WF_{varN}(Empty(i))) \equiv WF_{varN}(\exists i \in \mathcal{Z}_{N} : Empty(i))$$

3.5.2.  $\Box Inv \land \Box Type \land \Box [HRcv \lor HSnd]_{var} \Rightarrow$ WF<sub>(g,out)</sub>(Snd)  $\equiv$  WF<sub>varN</sub>( $\exists i \in \mathbb{Z}_N : Empty(i)$ ) 3.5.3. Q.E.D. PROOF: Steps 3.5.1 and 3.5.2.

We first consider step 3.5.1. When writing TLA specifications, one often has to choose between asserting fairness of  $A_1 \vee \ldots \vee A_m$  and asserting fairness of each action  $A_i$ . The choice becomes a matter of taste when the resulting specifications are equivalent. This is the case if, whenever one of the  $A_i$ becomes enabled, a step of no other  $A_j$  can occur before the next  $A_i$  step. For weak fairness, the equivalence is a consequence of the following result, which can be derived from the TLA proof rules of [5].

#### Lemma 3 If

 $ENABLED \langle \mathcal{A}_i \rangle_v \wedge \Box Inv \wedge \Box [\mathcal{N} \wedge \neg \mathcal{A}_i]_v \Rightarrow \Box \neg ENABLED \langle \mathcal{A}_j \rangle_v$ 

for all  $i, j \in S$  with  $i \neq j$ , then

$$\Box Inv \land \Box[\mathcal{N}]_v \Rightarrow (WF_v(\exists i \in S : \mathcal{A}_i) \equiv (\forall i \in S : WF_v(\mathcal{A}_i)))$$

We use this lemma to prove step 3.5.1.

3.5.1.  $\Box Inv \wedge \Phi_{\rm N}^{\rm hS} \Rightarrow$  $(\forall i \in \mathcal{Z}_N : WF_{varN}(Empty(i))) \equiv WF_{varN}(\exists i \in \mathcal{Z}_N : Empty(i)))$ 3.5.1.1.  $\Phi_{\mathbf{N}}^{\mathrm{hS}} \Rightarrow \Box[\exists i \in \mathcal{Z}_{N} : Fill(i) \lor Empty(i)]_{varN}$ **PROOF:** TLA2, since  $HFill(i) \Rightarrow Fill(i)$  and  $HEmpty(i) \Rightarrow Empty(i)$ .  $3.5.1.2. \land i \in \mathcal{Z}_N$  $\wedge$  IsNext(gg, i)  $\wedge \Box(Inv \wedge Type)$  $\wedge \Box [(\exists j \in \mathcal{Z}_N : Fill(j) \lor Empty(j)) \land \neg Empty(i)]_{varN}$  $\Rightarrow \Box IsNext(gg, i)$ **PROOF**: By rules INV1 and INV2, since  $Inv \land Type \land (Fill(j) \lor Empty(j)) \land \neg Empty(i)$ implies gg' = gg, for all  $i, j \in \mathbb{Z}_N$ .  $3.5.1.3. \land (i, j \in \mathbb{Z}_N) \land (i \neq j)$  $\wedge \text{ ENABLED } \langle Empty(i) \rangle_{varN}$  $\wedge \Box(Inv \wedge Type)$  $\wedge \Box [(\exists k \in \mathcal{Z}_N : Fill(k) \lor Empty(k)) \land \neg Empty(i)]_{varN}$  $\Rightarrow \Box \neg \text{Enabled} \langle Empty(j) \rangle_{varN}$ **PROOF:** Step 3.5.1.2 and rule STL4, since ENABLED  $\langle Empty(i) \rangle_{varN}$ implies IsNext(gg, i), and Lemma 1.1 implies  $Inv \wedge Type \wedge IsNext(gg, i) \Rightarrow \neg IsNext(gg, j)$ for all  $i, j \in \mathcal{Z}_N$  with  $i \neq j$ .

3.5.1.4. Q.E.D.

#### PROOF: Steps 3.5.1.1 and 3.5.1.3, and Lemma 3.

Finally, we prove 3.5.2, which completes the proof of the theorem.

3.5.2.  $\Box Inv \land \Box Type \land \Box [HRcv \lor HSnd]_{var} \Rightarrow$  $WF_{(q,out)}(Snd) \equiv WF_{varN}(\exists i \in \mathcal{Z}_N : Empty(i))$ 3.5.2.1.  $Inv \land Type \land [HRcv \lor HSnd]_{var} \Rightarrow$  $\langle Snd \rangle_{\langle g, out \rangle} \equiv \langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN}$ **PROOF:** By steps 3.1b and 3.2, since  $Inv \wedge Type \wedge [HRcv \vee HSnd]_{var}$ implies  $\langle Snd \rangle_{\langle q, out \rangle} \equiv HSnd$ , and  $Inv \wedge Type \wedge [\exists i \in \mathcal{Z}_N : HFill(i) \lor HEmpty(i)]_{var}$ implies  $\langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN} \equiv \langle \exists i \in \mathcal{Z}_N : HEmpty(i) \rangle$ . 3.5.2.2.  $\Box Inv \land \Box Type \land \Box [HRcv \lor HSnd]_{var} \Rightarrow$  $\Box \diamondsuit \langle Snd \rangle_{(g,out)} \equiv \Box \diamondsuit \langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN}$  $PROOF: \Box Inv \land \Box Type \land \Box [HRcv \lor HSnd]_{var}$  $\Rightarrow$  by 3.5.2.1 and rules STL5 and TLA2, since  $\langle A \rangle_v \equiv \neg [\neg A]_v$  $\Box[\neg Snd]_{(q,out)} \equiv \Box[\neg \exists i \in \mathcal{Z}_N : Empty(i)]_{varN}$  $\Rightarrow \neg \Box [\neg Snd]_{(g,out)} \equiv \neg \Box [\neg \exists i \in \mathcal{Z}_N : Empty(i)]_{varN}$  $\Rightarrow$  by rules STL3, STL4, and STL5  $\Box \neg \Box [\neg Snd]_{(g,out)} \equiv \Box \neg \Box [\neg \exists i \in \mathcal{Z}_N : Empty(i)]_{varN}$  $\Rightarrow$  since  $\Diamond \equiv \neg \Box \neg$  $\Box \Diamond \neg [\neg Snd]_{(g,out)} \equiv \Box \Diamond \neg [\neg \exists i \in \mathcal{Z}_N : Empty(i)]_{varN}$  $\Rightarrow$  since  $\langle A \rangle_v \equiv \neg [\neg A]_v$  $\Box \diamondsuit \langle Snd \rangle_{\langle g_{+}out \rangle} \equiv \Box \diamondsuit \langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN}$ 3.5.2.3  $\Box Inv \land \Box Type \land \Box [HRcv \lor HSnd]_{var} \Rightarrow$  $\Box \Diamond \neg \text{ENABLED} \langle Snd \rangle_{\langle q, out \rangle} \equiv$  $\Box \Diamond \neg \text{Enabled} \langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN}$ **PROOF:** Rules STL2 (which implies  $F \Rightarrow \Diamond F$ ), STL4, STL5, and TLA2, since by 3.5.2.1,  $Inv \wedge Type \wedge [HRcv \vee HSnd]_{var}$  implies ENABLED  $\langle Snd \rangle_{\langle g, out \rangle} \equiv \text{Enabled} \langle \exists i \in \mathcal{Z}_N : Empty(i) \rangle_{varN}$ 3.5.2.4 Q.E.D. **PROOF:** Steps 3.5.2.2 and 3.5.2.3, since  $WF_{\nu}(A)$  is defined to equal  $\Box \diamondsuit \neg \mathsf{E}\mathsf{NABLED} \langle A \rangle_v \lor \Box \diamondsuit \langle A \rangle_v.$ 

### 4 Further Remarks

We have proved the equivalence of two different representations of the ring buffer. This is not just an intellectual exercise; the ability to transform an algorithm into a completely different form is important for applying formal methods to real systems. Going from the two-process version to the N- process one reduces the internal state of each process from an unbounded number (p or g) to three bits (pp[i], gg[i], and ctl[i]). As explained in [3], such a transformation enables us to apply model checking to unbounded-state systems.

In retrospect, it is not surprising that programs with different numbers of processes can be equivalent. Multiprocess programs are routinely executed on single-processor computers by interleaving the execution of their processes. The transformation of  $\Phi_2$  and  $\Phi_N$  to  $\Phi_2^u$  and  $\Phi_N^u$  can be viewed as a formal description of this interleaving.

Using an interleaving representation makes the proof of equivalence a bit simpler, but it is not necessary. The equivalence of noninterleaving representations can be proved as follows. Let RcvNI and SndNI be the actions obtained from Rcv and Snd by removing the UNCHANGED conjuncts and adding the conjunct UNCHANGED  $UnB(p \mod N)$  to RevNI. Replacing Rev and Snd with RevNI and SndNI in the definition of  $\Pi_2$  yields a noninterleaving representation of the two-process program. Similarly, we get a noninterleaving representation of the N-process program by replacing Fill(i) and Empty(i) with actions FillNI(i) and EmptyNi(i) that have no UNCHANGED conjuncts except the one for UnB(i). In the proof of equivalence, formula  $\Phi_2^{\rm u}$  is changed by replacing its next-state action  $Rcv \vee Snd$ with  $Rcv \lor Snd \lor (RcvNI \land SndNI)$ , and  $\Phi_N^u$  is changed by replacing its nextstate action with  $\exists i \in \mathbb{Z}_N$ :  $Fill(i) \lor Empty(i) \lor (FillNI(i) \land EmptyNI(i))$ . Formulas  $\Phi_2^h$  and  $\Phi_N^h$  are obtained by adding history variables to the new versions of  $\Phi_2^{\rm u}$  and  $\Phi_{\rm N}^{\rm u}$ . The proof of equivalence is the same as before, except we have to consider the next-state actions' extra disjuncts. These disjuncts represent the simultaneous sending and receiving of values.

Indivisible state changes are an abstraction; executing an operation of a real program takes time. In TLA, we can represent the concurrent execution of program operations either as successive steps, or as a single step. Which representation we choose is a matter of convenience, not philosophy. We have found that interleaving representations are usually, but not always, more convenient than noninterleaving ones for reasoning about algorithms.

A proof that two algorithms are equivalent can be turned into a derivation of one algorithm from the other. Our proof yields the following derivation, where each equivalence is obtained from the indicated proof step(s).

$$\begin{split} \Pi_2 &\equiv & \mathbf{\exists} : p, g : \Phi_2^{\mathrm{u}} & \qquad \mathbf{1a} \\ &\equiv & \mathbf{\exists} \, p, g, pp, gg, ctl : \Phi_2^{\mathrm{h}} & \qquad \mathbf{2a} \end{split}$$

$\equiv$	$\exists p, g, pp, gg, ctl : \Phi_2^{\rm h} \land \Box Inv$	3.3a
Ξ	$\exists pp, gg, ctl, p, g : \Phi_{\mathrm{N}}^{\mathrm{h}} \land \Box Inv$	3.4 and $3.5$
Ξ	$\exists pp, gg, ctl, p, g : \Phi^{h}_{N}$	3.3b
Ξ	$\exists p, g : \Phi_{\mathrm{N}}^{\mathrm{u}}$	$2\mathrm{b}$
Ξ	$\exists p, g : \Phi_{\mathrm{N}}^{\mathrm{u}} \wedge \Box InvN$	1b.1b
≡	$\exists p, g : \Phi_{\mathrm{N}} \land \Box InvN$	1b.3
$\equiv$	$\Pi_{N}$	1b.1a

Our derivation uses rules of logic to rewrite formulas. In process algebra [6], analogous transformations are performed by applying algebraic laws. It would be interesting to compare a process-algebraic proof of equivalence of the two ring-buffer programs with our TLA proof.

## A Proof of the Theorem

```
Theorem \Pi_2 \equiv \Pi_N
1a. \Phi_2 \equiv \Phi_2^u
       1a.1. Type2 \Rightarrow ([Rcv]_{(p,buf,in)} \land [Snd]_{(g,out)} \equiv [Rcv \lor Snd]_{var2})
       1a.2. \ \Box \ Type2 \Rightarrow (\Box [Rcv]_{(p, buf, in)} \land \Box [Snd]_{(g, out)} \equiv \Box [Rcv \lor Snd]_{var2})
1b. \Phi_N \equiv \Phi_N^u
       1b.1a. \Phi_N \Rightarrow \Box InvN
                  1b.1a.1 TypeN \land (\forall i \in \mathcal{Z}_N : pp[i] = gg[i] = 0) \Rightarrow InvN
                  1b.1a.2 \wedge InvN
                               \wedge [TypeN \land (\forall i \in \mathcal{Z}_N : Fill(i) \lor Empty(i) \lor NotProc(i))]_{varN}
                               \Rightarrow InvN'
                  1b.1a.3. \land TypeN \land (\forall i \in \mathbb{Z}_N : pp[i] = gg[i] = 0)
                                \wedge \Box TypeN \land \Box [\forall i \in \mathcal{Z}_N : Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}
                               \Rightarrow \Box InvN
       1b.1b. \Phi_{N}^{u} \Rightarrow \Box InvN
                  1b.1b.1. TypeN \land (\forall i \in \mathcal{Z}_N : pp[i] = gg[i] = 0) \Rightarrow InvN
                  1b.1b.2. \wedge InvN
                                 \wedge [TypeN \land (\exists i \in \mathcal{Z}_N : Fill(i) \lor Empty(i)]_{varN}
                                 \Rightarrow InvN'
                  1b.1b.3. \land TypeN \land (\forall i \in \mathbb{Z}_N : pp[i] = gg[i] = 0)
                                 \wedge \Box TypeN \land \Box [\exists i \in \mathcal{Z}_N Fill(i) \lor Empty(i)]_{varN}
                                 \Rightarrow InvN'
       1b.2. TypeN \land InvN \Rightarrow
                     [\exists i \in \mathcal{Z}_N : Fill(i) \lor Empty(i)]_{varN} \equiv
                          \forall i \in \mathcal{Z}_N : [Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}
```

1b.3.  $\Box$  TypeN  $\land \Box$  InvN  $\Rightarrow$  $\Box[\exists i \in \mathcal{Z}_N : Fill(i) \lor Empty(i)]_{varN} \equiv$  $\forall i \in \mathcal{Z}_N : \Box[Fill(i) \lor Empty(i) \lor NotProc(i)]_{varN}$ 2a.  $\Phi_2^{\rm u} \equiv \exists pp, gg, ctl : \Phi_2^{\rm h}$ b.  $\Phi_{\mathrm{N}}^{\mathrm{u}} \equiv \exists p, g : \Phi_{\mathrm{N}}^{\mathrm{h}}$ 3.  $\Phi_2^h \equiv \Phi_N^h$ 3.1a.  $Type \land Inv \Rightarrow (HRcv \equiv \exists i \in \mathcal{Z}_N : HFill(i))$ 3.1a.1  $Type \land Inv \Rightarrow (HRcv \equiv HFill(p \mod N))$ 3.1a.1.1.  $Type \wedge Inv \Rightarrow ((p - g \neq N) \equiv (ctl[p \mod N] = "empty"))$ 3.1a.1.2. Type  $\land$  Inv  $\Rightarrow$  IsNext(pp, p mod N) 3.1a.2  $Type \land Inv \Rightarrow (HFill(p \mod N) \equiv (\exists i \in \mathcal{Z}_N : HFill(i)))$ 3.1b.  $Type \land Inv \Rightarrow (HSnd \equiv \exists i \in \mathcal{Z}_N : HEmpty(i))$ 3.1b.1  $Type \land Inv \Rightarrow (HSnd \equiv HEmpty(g \mod N))$ 3.1b.1.1.  $Type \land Inv \Rightarrow ((p - g \neq 0) \equiv (ctl[g \mod N] = "empty"))$ 3.1b.1.2.  $Type \land Inv \Rightarrow IsNext(gg, g \mod N)$ 3.1b.1  $Type \land Inv \Rightarrow (HEmpty(p \mod N) \equiv (\exists i \in \mathcal{Z}_N : HEmpty(i)))$ 3.2.  $[Type \land Inv \land (HRcv \lor HSnd)]_{var} \equiv$  $[Type \land Inv \land (\exists i \in \mathcal{Z}_N : HFill(i) \lor HEmpty(i))]_{var}$ 3.3a.  $\Phi_2^{hS} \Rightarrow \Box Inv$ b.  $\Phi_{N}^{hS} \Rightarrow \Box Inv$ 3.3.1.  $Init \Rightarrow Inv$ 3.3.2a.  $Inv \wedge [Type \wedge (HRcv \lor HSnd)]_{var} \Rightarrow Inv'$ 3.3.2a.1  $Inv \wedge Type \wedge HRcv \Rightarrow Inv'$ 3.3.2a.2  $Inv \wedge Type \wedge HSnd \Rightarrow Inv'$ 3.3.2a.3  $Inv \wedge (var' = var) \Rightarrow Inv'$ 3.3.2b.  $Inv \wedge [Type \wedge (\exists i \in \mathbb{Z}_N : HFill(i) \lor HEmpty(i))]_{var} \Rightarrow Inv'$ 3.4.  $\Phi_2^{hS} \equiv \Phi_N^{hS}$ 3.5.  $\Box Inv \wedge \Phi_{N}^{hS} \Rightarrow (WF_{(g,out)}(Snd) \equiv (\forall i \in \mathcal{Z}_{N} : WF_{varN}(Empty(i))))$ 3.5.1.  $\Box Inv \wedge \Phi_{N}^{hS} \Rightarrow$  $(\forall i \in \mathcal{Z}_N : WF_{varN}(Empty(i))) \equiv WF_{varN}(\exists i \in \mathcal{Z}_N : Empty(i)))$ 3.5.1.1.  $\Phi_{N}^{hS} \Rightarrow \Box[\exists i \in \mathcal{Z}_{N} : Fill(i) \lor Empty(i)]_{varN}$ 3.5.1.2.  $\land i \in \mathbb{Z}_N$  $\wedge$  IsNext(gg, i)  $\wedge \Box(Inv \wedge Type)$  $\wedge \Box [(\exists j \in \mathcal{Z}_N : Fill(j) \lor Empty(j)) \land \neg Empty(i)]_{varN}$  $\Rightarrow \Box IsNext(gg, i)$ 

 $\begin{array}{l} 3.5.1.3. \land (i, j \in \mathcal{Z}_N) \land (i \neq j) \\ \land \text{ ENABLED } \langle \textit{Empty}(i) \rangle_{varN} \\ \land \Box(\textit{Inv} \land \textit{Type}) \\ \land \Box[(\exists k \in \mathcal{Z}_N : \textit{Fill}(k) \lor \textit{Empty}(k)) \land \neg\textit{Empty}(i)]_{varN} \\ \Rightarrow \Box \neg \text{ENABLED } \langle \textit{Empty}(j) \rangle_{varN} \\ 3.5.2. \Box \textit{Inv} \land \Box \textit{Type} \land \Box[\textit{HRcv} \lor \textit{HSnd}]_{var} \Rightarrow \\ \text{WF}_{\{g,out\}}(\textit{Snd}) \equiv \text{WF}_{varN}(\exists i \in \mathcal{Z}_N : \textit{Empty}(i)) \\ 3.5.2.1. \textit{Inv} \land \textit{Type} \land [\textit{HRcv} \lor \textit{HSnd}]_{var} \Rightarrow \\ \langle \textit{Snd} \rangle_{\{g,out\}} \equiv \langle \exists i \in \mathcal{Z}_N : \textit{Empty}(i) \rangle_{varN} \\ 3.5.2.2. \Box \textit{Inv} \land \Box \textit{Type} \land \Box[\textit{HRcv} \lor \textit{HSnd}]_{var} \Rightarrow \\ \Box \diamond \langle \textit{Snd} \rangle_{\{g,out\}} \equiv \Box \diamond \langle \exists i \in \mathcal{Z}_N : \textit{Empty}(i) \rangle_{varN} \\ 3.5.2.3 \Box \textit{Inv} \land \Box \textit{Type} \land \Box[\textit{HRcv} \lor \textit{HSnd}]_{var} \Rightarrow \\ \Box \diamond \neg \textit{ENABLED} \langle \textit{Snd} \rangle_{\{g,out\}} \equiv \\ \Box \diamond \neg \textit{ENABLED} \langle \textit{Snd} \rangle_{\{g,out\}} \equiv \\ \Box \diamond \neg \textit{ENABLED} \langle \exists i \in \mathcal{Z}_N : \textit{Empty}(i) \rangle_{varN} \end{array}$ 

## **B** Proof of Lemma 1

**Lemma 1** If  $m \in \mathcal{N}$  and  $i \in \mathcal{Z}_N$ , then

- IsNext(Rep(m), i) ≡ (i = m mod N)
   IsNext(Rep(m), i) ⇒ Rep(m + 1) = [Rep(m) EXCEPT ![i] = 1 - Rep(m)[i]]
- 1.  $Rep(m + 1) = [Rep(m) \text{ EXCEPT } ! [m \mod N] = 1 Rep(m)[m \mod N]]$ 1.1. CASE:  $(m+1) \mod 2N = (m \mod 2N) + 1$ PROOF: It suffices to prove that

$$Rep(m+1)[j] = if j = m \mod N then 1 - Rep(m)[j]$$
  
else  $Rep(m)[j]$ 

for any  $j \in \mathcal{Z}_N$ . The proof follows.

- 1.1.1.  $(j \equiv m \mod N) \equiv (j \equiv m \mod 2N) \lor (j + N \equiv m \mod 2N)$ PROOF: Simple number theory.
- 1.1.2. If  $j = m \mod N$ , then  $(j < 1 + (m \mod 2N) \le j + N) \equiv \neg (j < m \mod 2N \le j + N)$ PROOF: Step 1.1.1 and simple arithmetic.

1.1.3. If  $j \neq m \mod N$  then  $(j < 1 + (m \mod 2N) \leq j + N) \equiv (j < m \mod 2N \leq j + N)$ PROOF: Step 1.1.1 and simple arithmetic.

1.1.4. Q.E.D.

**PROOF:** By 1.1.2, 1.1.3, and the definition of Rep.

1.2. CASE:  $(m+1) \mod 2N \neq (m \mod 2N) + 1$ **PROOF:** The case assumption implies  $m \mod 2N = 2N - 1$ , which implies  $= [i \in \mathcal{Z}_N \mapsto if i = N - 1 then 0 else 1]$ Rep(m) $Rep(m+1) = [i \in \mathcal{Z}_N \mapsto 0]$ 1.3. Q.E.D. PROOF: Steps 1.1 and 1.2. 2.  $IsNext(Rep(m), i) \equiv (i = m \mod N)$ The proof is by induction on m. 2.1. Case: m = 0**PROOF:**  $Rep(0) = [j \in \mathcal{Z}_N \mapsto 0]$  and  $IsNext(Rep(0), i) \equiv (i = 0)$ , for  $i \in \mathcal{Z}_N$ . 2.2. ASSUME:  $IsNext(Rep(m), i) \equiv (i = m \mod N)$ PROVE:  $IsNext(Rep(m+1), i) \equiv (i = m + 1 \mod N)$ The result is trivial if N = 1. We assume N > 1. 2.2.1.  $(i \equiv m \mod N) \Rightarrow (IsNext(Rep(m+1), i) \equiv \neg IsNext(Rep(m), i))$ **PROOF**:  $i = m \mod N$  implies IsNext(Rep(m + 1), i) $\equiv$  by definition of *IsNext* if i = 0 then Rep(m+1)[0] = Rep(m+1)[N-1]**else**  $Rep(m+1)[i] \neq Rep(m+1)[i-1]$  $\equiv$  by step 1 if i = 0 then 1 - Rep(m)[0] = Rep(m)[N-1]else  $1 - \operatorname{Rep}(m)[i] \neq \operatorname{Rep}(m)[i-1]$  $\equiv \neg IsNext(Rep(m), i)$ 2.2.2.  $(i = m+1 \mod N) \Rightarrow (IsNext(Rep(m+1), i) \equiv \neg IsNext(Rep(m), i))$ **PROOF:**  $i = m + 1 \mod N$  implies IsNext(Rep(m + 1), i) $\equiv$  by definition of *IsNext* if i = 0 then Rep(m+1)[0] = Rep(m+1)[N-1]else  $Rep(m+1)[i] \neq Rep(m+1)[i-1]$  $\equiv$  by step 1, since N > 1 implies  $m + 1 \mod N \neq m \mod N$ if i = 0 then Rep(m)[0] = 1 - Rep(m)[N-1]else  $Rep(m)[i] \neq 1 - Rep(m)[i-1]$  $\equiv \neg IsNext(Rep(m), i)$ 2.2.3.  $(i \neq m \mod N) \land (i \neq m+1 \mod N) \Rightarrow$  $IsNext(Rep(m + 1), i) \equiv IsNext(Rep(m), i)$ **PROOF:** The hypothesis implies IsNext(Rep(m + 1), i)

= by definition of IsNextif i = 0 then <math>Rep(m + 1)[0] = Rep(m + 1)[N - 1]else  $Rep(m + 1)[i] \neq Rep(m + 1)[i - 1]$ = by step 1if i = 0 then <math>Rep(m)[0] = Rep(m)[N - 1]else  $Rep(m)[i] \neq Rep(m)[i - 1]$ = IsNext(Rep(m), i)2.2.4. Q.E.D. PROOF: By 2.2.1-2.2.3 and the induction assumption.

2.3 Q.E.D.

**PROOF**: By steps 2.1 and 2.2 and mathematical induction.

- 3.  $IsNext(Rep(m), i) \Rightarrow (Rep(m + 1) = [Rep(m) \text{ EXCEPT } ![i] = 1 Rep(m)[i]])$ PROOF: Immediate from steps 1 and 2.
- 4. Q.E.D.

PROOF: Steps 2 and 3.

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